

Meeting Participants

Penticton	Amy, Dave DR, Donna, Gary, Mark, Ralph, Sonja, Zhang, Zoran
Socorro	Allen, Bill, Brent, Bruce, Bryan, John, Ken, Kevin, Martin, Michael
Jodrell Bank	Bryan

Software Focus

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|-----------------------|---|
| Sonja Vrcic | <ul style="list-style-type: none">• Sonja spoke with Karen Fort re: Dave's status. He will be unavailable for work for several weeks.<ul style="list-style-type: none">→ Dave suffered a minor heart attack over the weekend and requires tests.→ Action – Sonja to let Brent know when Dave goes to Kelowna for tests.• Working on the presentation for the VCI review scheduled for July 10 and 11.<ul style="list-style-type: none">→ Once complete, Sonja will forward the presentation to Michael for comment.• Making some progress re: config mapper and code. |
| Dave Del Rizzo | <ul style="list-style-type: none">• Working on the RTDD.<ul style="list-style-type: none">→ Received a call from Brent in Socorro yesterday: the data valid seems to be applied twice.→ Made some changes re: implementing the Baseline Board, now needs to back code to implement the changes.→ Should have a new version to use with the Station Board by the end of this week.→ Will update the JNL file. Connecting to boards connecting will be different from previously.→ Everything is plotting fine. The RTDD will be usable for the Baseline Board by the end of next week.• Three values are high but post-re-quantizing is to be separate; there will be two windows, each with its own scale.• Brent authorized Dave to work overtime if needed. |
| Brent Carlson | <ul style="list-style-type: none">• Brent's utility programs are installed and running at the VLA. |
| Kevin Ryan | <ul style="list-style-type: none">• Copied the JNL file.• Getting the test executor suite of GUIs ready.<ul style="list-style-type: none">→ Trying to get file systems working in an organized manner.→ Encountered one issue so will request Tony H make a similar directory on Amy01. |
| Bruce Rowen | <ul style="list-style-type: none">• Continuing with test vector testing.• Implementing parts of Kevin's front panel to include temperature and voltage.• Sonja inquired re: dump trig version.<ul style="list-style-type: none">→ Bruce commented that he implemented the version that is in the VCI configuration. |
| Martin Pokorny | <ul style="list-style-type: none">• PC and CBE computers running with the software and are working fine. Working towards writing BDF files. |
| Michael Rupen | <ul style="list-style-type: none">• Working with Martin and Rich re: inputting fake SDM and BDF into CASA and AIPS.• Preparing for the VCI review scheduled for Thursday and Friday afternoons. |

- Assembled a prototype correlator software documentation website: will distribute an email. Comments welcome.
- Setting up a morning/day of software demonstration for next week while Brent is at Socorro.
 - Brent commented that it could be helpful to have Sonja attend in Dave's absence.
 - Sonja is available to travel to Socorro this weekend if necessary.
 - **Action** – Michael to determine if Sonja needs to attend the software demonstration in Socorro.
 - Sonja inquired re: each CMIB having its own website.
 - There is no need to launch the GUIs from the CMIB. People can be re-directed to the server.
 - There will be password protection within a few months.

New Software Action Items

- Sonja to let Brent know when Dave goes to Kelowna for tests.
- Michael to determine if Sonja needs to attend the software demonstration in Socorro next week.

Continuing Software Action Items

- Kevin to forward an email to Ralph re: the RPMIB and auto refresh.
- Mark to order bigger ESD bags for the next board shipment.
- Kevin to forward the new code that was programmed into the NRAO board that originates the time code to send to the Fanout Board to Brent for testing at DRAO to proceed.
- Bruce to set the obs time to synchronize re: Station Board to Baseline Board testing.
- Kevin to build a delay button into the GUI to accommodate different time requirements.
- Bruce to forward the artificial Linux distribution system created by James to DRAO.
- Brent to consider possibilities for testing the CPCC and let Kevin know.
- Kevin to check re: if each board is identified with a serial number on the board.
- Martin to forward a copy of the CBE schema/config to Sonja once a stable copy is available.
- Brent to review the 4-bit and 7-bit correlation samples for the VCI as Sonja would like to ensure that the connections/syntax are agreed upon.
- Michael to clarify with Bryan re: Delay Model, on-the-fly observing, smoothing, buffering, and cuing and report back to Hichem and Bruce.
- Michael to define critical tests.

Hardware Update

Brent Carlson

- Brent reported that the changes to the Delay Module paid off well.
 - Getting cross correlation fringes but seeing some strange behaviour. Source code is not correlating.
 - **Action** – Zoran to investigate the correlating issue.
 - Zoran reported issues with high delay rates; there could be a problem with test vectors.
 - The higher end of the band may not be functioning correctly. Lower rates are perfectly fine.
 - Tests are to begin at the VLA today followed by a transition to real data from the antennas.
- Bruce reported a chip on the PC104+ acting funny and wondered if it is the chip that was replaced previously.

- Brent suggested that the chip be replaced when the power supply is replaced.
- Encountered a time-code synchronization bug; investigation is in progress.
- Zoran Ljusic**
 - Nothing to report.
- Zhang Heng**
 - The Baseline Board files were submitted June 27 to BMS for DFM, DFT and quote.
 - Amy received notice that the quote was started on June 30 and should be ready by August 14. BMS is uncertain if the DFM and DFT will be done by then.
 - Working on the Cross-bar board design. Expects to be finished by early August.
 - Completed the schematics. Working on placement. Added the ferrite beads and the filter for the PRO.
 - The layout of the drivers on the output of the FPGA buffer; it is doable.
- Ralph Webber**
 - Ralph reported that Dale has completed the installation of the Meritec cabling for two of the Station Racks.
 - The RPMIBs have been tested.
 - Crating has not been started. The target date for the truck to arrive at DRAO is July 20.
 - Crating the fans has not been started, just need to unscrew the fans and load them on to the pallets.
 - Also ready to ship the super logics analyzer, and other electronic equipment.
 - Brent emphasized the need to work towards the July 20 target date.
 - Of the 14 Station Boards ordered, 13 boards have been delivered to DRAO.
 - In Dave's absence, Zoran is to run the tests on the Station Board. The MITR dBase is set up for the tests.
 - Brent would like 2 boards shipped to Socorro as soon as they are tested although it is recognized that it is important that the boards are tested thoroughly.
 - Zoran commented that the ship date will depend on the level of confidence in the test results.
 - Brent would like that the two Station Boards arrive at the VLA in early August.
 - Amy reported that the Diemaster boxes have not yet arrived. Brent to follow up with an email.
- Mark Halman**
 - In the process of completing the heatsink design.
 - Brent commented that it will take a few months to receive the boards so there is some time.
 - It is important to get the panels ordered to be delivered at the same time as the boards are delivered.
 - The long-pin power supplies arrived; Mark will expedite their shipment to the AOC today.
 - Mark reported the contact at SeaAir has agreed that the racks can be shipped on their backs.
 - The crating material arrived yesterday so crating can begin.
 - Mark has not received any responses from other shipping companies.

Project Management

- Amy Fink**
 - Five of the Delay Module Boards are outstanding; BMS is waiting for connectors.
 - Received a quote for the Patch Board but it was identical to the previous one with only the date changed.
 - Amy is waiting for one file to complete the quote.