

Meeting Participants

Penticton	Dave F, Donna, Gary, Igor, Mark, Ralph, Sean, Sonja, Zoran
Socorro	Allen, Bill, Brent, Bruce, Bryan, Kevin, Mark, Martin, Michael
Jodrell Bank	Bryan, Christine, Roger, Simon

Software Focus

- Sonja Vrcic**
- Working on code for the configuration mapper.
 - Sonja is not quite ready to publish an updated VCI document.
 - Preparing for the review.
 - Igor worked on the SB GUI and fixed and/or updated tasks as assigned. He is waiting for definition of schema.
 - Updates are not yet fully tested as waiting for Bruce and Dave to finish sorting out issues.
 - Sonja requested notification re: SB use in the lab to include both Igor and Sonja.
 - **Action** – Sonja to forward Igor's email address to Brent.
- Dave Fort**
- Developed a new test re: preliminary test with only the SB and spectral lines (no noise), one each per sub-band.
 - Can determine functionality re: Delay Modules, etc.
 - Yesterday, resurrected the 3rd SB which seems okay for testing at this point.
 - Brent commented that the SB connected to the BB on the bench permits test capability.
 - May need to hook up the logic analyser to figure out signalling to correlator chip.
 - As long as the original BB is behaving exactly the same as the ones at the VLA, and the SB is in the test rack on the bench, the boards can be shipped to Socorro.
 - Bruce indicated that synchronization is not yet complete.
 - Brent commented that the boards cannot be shipped until the synchronization is done.
 - **Action** – Bruce to set the obs time to synchronize re: SB to BB testing.
 - Dave indicated there could be significant down time if the boards are shipped too soon.
 - Brent reported that network speed connecting Socorro to Penticton is hampering some of the current testing.
 - Brent requested that the boards in test rack be left as is for the whole day, today.
 - Some cosmetic issues remain, e.g. fixing the LED on the SB.
- Dave Del Rizzo**
- Updated the MITR dBase.
 - Was able to successfully read in a BLS file. Thanks for Martin for updating the BLS document.
- Brent Carlson**
- Generated config file for BB.
 - **Action** – Brent to send the config file for the BB to Sonja.
 - Able to open the GUI PCP file but has not yet tested it.
 - Getting 12 sub-bands, 1K channels apiece out of 1 baseline board using the test rack in Penticton identical to the one at Socorro.

- Generated a work sheet re: figuring out various configurations for testing.
- Kevin Ryan**
- Working on the test executor suite software.
 - Implemented the Jira task from Dave F.
 - Dave to test the task and close the task on Jira if satisfied.
 - Barry and Kevin worked together re: DM data flowing from the test executor into the CMIB located in the office.
 - Comment from Kevin re: issues connecting to the SB Java launching point to Socorro.
 - **Action** – Sonja to move SB Java launching point to Socorro.
 - Brent recorded spam in excess of 1000 items last night.
 - **Action** – Donna to remove email addresses from the public page of the WIDAR website. [done]
- Bruce Rowen**
- Testing with Brent.
 - Reviewed Jira items.
 - Updated schema and power supplies, etc.
 - Posted new schema to web.
 - Bruce commented that he doesn't think there are any unadvertised changes.
- Martin Pokorny**
- Updated and forwarded a new version of the BLS document for posting to the WIDAR website.
 - Working with Rich Moeser re: SDM and writing BDF files re: work arounds for the ALMA browser.
 - This is a slow process, would like someone from NRAO to help with de-bugging.
 - Integrating BLS with BDF software.
 - James has two nodes ready re: CBE. Martin is working on the configuration.
- Michael Rupen**
- Moved to Socorro on Friday.
 - Working with George re: filler question re: reading SDM.
 - Need the writer from CASA so it can write what AIPs can read.
 - ALMA and delay in definitions should be available by June 16.
 - Attended a meeting re: science and critical tests and concerns, etc, for signing off on OTS testing.
 - Comments are being implemented with the results to be distributed next week.
- Mark McKinnon**
- Thanks to Brent and Dale re: organization and planning for installation of first 8 racks.
 - A camera is installed in the room; there will be a video recording available of installation.
 - The NRAO webpage has been popular re: correlator installation.

New Software Action Items

- Sonja to forward Igor's email address to Brent.
- Bruce to set the obs time to synchronize re: SB to BB testing.
- Brent to send the config file for the BB to Sonja.

Continuing Software Action Items

- Bruce to forward Rich Moeser's schemas to Sonja.
- Bruce to investigate and identify date formats to be used.
- Kevin to build a delay button into the GUI to accommodate different time requirements.
- Bruce to forward the artificial Linux distribution system created by James to DRAO.
- Sonja to initiate an email discussion to decide where messages will be intercepted and passed on.
- Bruce to forward direction to Dave F re: GNU GRUB file and boot up problems.
- Brent to consider possibilities for testing the CPCC and let Kevin know.
- Kevin to check re: if each board is identified with a serial number on the board.
- NRAO to clarify their schedule within the next few weeks re: 4-station prototype correlator and inform DRAO.
- Bruce to come up with a directory structure to support start up sequencer files.
- Martin to forward a copy of the CBE schema/config to Sonja once a stable copy is available.
- Brent to review the 4-bit and 7-bit correlation samples for the VCI as Sonja would like to ensure that the connections/syntax are agreed upon.
- Michael to clarify with Bryan re: Delay Model, on-the-fly observing, smoothing, buffering, and cuing and report back to Hichem and Bruce.
- Michael to define critical tests.

Hardware Update – no formal Hardware Update Session occurred this week.

Brent Carlson

- All racks from the first shipment were received undamaged and have been installed.
 - They are bolted together and to the floor.
 - The Meritec cables are in the process of being installed.
 - The cable trays are not yet started.
- Brent reported that Bob Broilo will request a purchase of items for the power plants.
 - Ralph is to expedite the order with Emerson.
- Brent reported that he is splitting his work time between the VLA and Socorro.
 - Working with Bruce re: issues as they are encountered.
- The prototype correlator is installed in the old correlator room and hooked up to power.
 - Encountered one issue with continuous CRC errors.
 - Once the SB arrives at Socorro, the problem will be sorted out easily.
 - If needed, could proceed with initial testing using the time-code generator board.
- Zoran reported that due to the power outage and subsequent network disruption Tuesday at DRAO, no progress was made on the Baseline Board.
 - Brent reported that on Friday, every change for the BB was checked in detail Brent and Zhang.
 - Just the silkscreen marking was left to do.
 - Everything is ready to forward to BMS for quotation and DFM.

- There has been no notification from BMS re: Station Board delivery.
 - Zoran reported in Amy's absence that BMS was performing flying probe and JTAG testing.
 - Zoran anticipates delivery next week.
- Testing of the eMERLIN racks is complete. They can be crated in preparation for shipping.
 - **Action** – Mark H to email Simon re: shipping company information so that shipping details can be finalized.
- MuAnalysis continue to test the correlator chips.
 - Brent to follow up re: performing screen testing at MuAnalysis.
 - Ralph forwarded more testers to MuAnalysis.
- Simon inquired re: Will the Station Board for eMERLIN have VSI chips installed?
 - Dave reported that the current plan is to have chips installed on the boards at DRAO.

Dave Fort