Meeting Date	2006January04
Meeting Participants	
Penticton	Amy, Donna, Mark, Ralph, Zhang, Zoran
Socorro	Bruce, Kevin
Jodrell Bank	Bryan, Dave, Simon
Penticton	
Zoran Ljusic	 Participated in the Baseline Board Design Review. Changes to the Station Board design are required as a result of the Baseline Board review and demands of BMS technologies: these changes include re-routing traces that were done with the auto-router.
Zhang Heng	 Completed the Baseline Board Design and close to four full days of review before Christmas. → Will take a couple of weeks for changes and simulation that resulted from the design review. → Once done, will move on to Baseline Board PCB Signal Integrity Analysis.
Ralph Webber	 Participated in the Baseline Board Design Review. BGA Socket design is complete; waiting for quotes.
Design Review	 → BGA Socket Design Review scheduled for 2:00 pm, Thursday, January 5, 2006. Investigated adhesives for between the elastomer and the board; there are some issues. → Needs to order a catalyst for the adhesive to see if that will help. Next tasks include specs documents and mechanical drawings.
Mark Halman	 Participated in the Baseline Board Design Review. Regenerated land pattern for Zhang but the file is not readily accepted by Mentor Graphics. Having the same problem in finding an acceptable file format for the WIDAR logo.
Amy Fink	 The budget is almost up-to-date; waiting for a few numbers from Brent. → Reviewed as is with Peter.
Socorro	
Bruce Rowen	Completed all of the MAHs.Next task is the Station Board software for the Time Code Generator and optimization for the MAHs.
Kevin Ryan	 Approximately 98% complete the GBit Ethernet GUI; will complete implementing Brent's changes next week. Working on web deployment; should take 1 more week then will finish off the GBit Ethernet GUI.
Iodrell Bank	
Simon Corrington	• Would be useful to the updated budget for the eMERLIN telecon scheduled for 0900/1700, 05Jan06.