

EVLA Weekly Meeting Notes

Meeting Date 2005December 07

Meeting Participants

Penticton	Amy, Brent, Dave, Donna, Mark, Peter, Rick, Sonja, Zhang, Zoran
Socorro	Bruce, Kevin
Jodrell Bank	Bryan, Simon

Penticton

- | | |
|----------------------|--|
| Brent Carlson | <ul style="list-style-type: none">• EVLA Budget decisions were made last week re: fiscal spending from now until March 31, 2005.<ul style="list-style-type: none">→ Earliest delivery is now April 2006, however, it is unlikely that this will cause a delay.→ It is taking twice as long as the original best estimates to complete the design work. Part of this is because iSine (Innotech) is behind by 6-7 months. There have been tool uncertainties, delays caused by contract constraints, and re-design issues.→ Need to do as much as possible to prevent further slippages. |
| Kevin – Question | → Will this impact any contract NRAO may have with Tom Morgan for testing? |
| Brent – Answer | <ul style="list-style-type: none">→ Yes, we probably won't need Tom until at least May, 2006 at the earliest.→ Re-spin is already built in to Stage 2; this would only involve design bugs, no functional changes.→ We are moving into a period of uncertainty; long review meetings are painful but necessary. |
| | <ul style="list-style-type: none">• Should receive the Gate-level Netlist by December 13.<ul style="list-style-type: none">→ Model SimSE license expires December 25. May need to pressure iSine re: no more slippages.→ Some time will be lost due to Christmas shutdown at DRAO from December 23 to January 3, 2006. This is not accounted for in the schedule.• Worked on correlator chip testing.<ul style="list-style-type: none">→ Reviewed validation report and the gate-level verification results earlier this week.<ul style="list-style-type: none">○ Will write a report that includes a complete matrix to prove adequate test coverage.○ Rick will edit his report to reflect the test cases completed.• Computer crashed, possibly due to over-heating in the RFI boxes. Took a few days to get a replacement.• Generated VCD file – 48Gbits: in the process of compressing the file. |
| Dave Fort | <ul style="list-style-type: none">• Was away for a few days.• Worked on the testing matrix, GUIs and made some changes to the filter chip.• Worked on a new scheme for functionality testing of the Station Board. |
| Zoran Ljusic | <ul style="list-style-type: none">• Worked on Station Board routing.<ul style="list-style-type: none">→ Had a couple of good runs last week.→ Refining the design. |

- Zhang Heng**
 - Almost finished routing the Baseline Board.
 - A review meeting is scheduled for December 16.
- Sonja Vrcic**
 - Made minor changes to the FPGA GUI.
- Rick Smegal**
 - Correlator chip test and verification review meeting was held Monday, December 5.
 - Minor changes to be made; will complete updating the reports by Friday, December 9.
 - Tony Hoffman is pressuring Rick to finish up so that he can perform some upgrades on the computer.
- Ralph Webber**
 - Brent reported that Ralph has sent out the BGA socket design for quotation.
 - Rolling ESD tool boxes cost ~ \$2000 each.
- Mark
 - There is a source in Montreal that may be cheaper than Stanley. Alternatively, we could buy the wheels and install them ourselves.
- Brent
 - Apparently, a supportive plate needs to be installed which is adding to the cost.
 - Brent will inquire of Gary H. and Tom B. re: do we really need rolling tool boxes.
- Mark
 - There are also dangly tails that may work.
- Mark Halman**
Brent
 - Existing wrist straps don't fit on the Jewel Monitor boxes.
 - Order 10 more wrist straps of the right size.
 - Updated the soldering station with electrostatic bench-top.
 - Received a quote for machining the heat sink:
 - Production costs are ~ \$180CD/each for machining only for a total of \$275-\$300 USD total.
 - This is significantly lower than the monolithic heat sink once considered.
 - Passed machining contact info on to Ralph.
 - Continued investigation into Indium: it will cost \$.66/each, cut to the size we need.
- Peter Dewdney**
 - Hopefully the contract will be signed this week, but there is a possibility that it may not be.
 - Recently revised the pricing formula, which took some time.
 - The employment equity issues have been resolved.
 - Signing the contract is not on the critical path.
- Amy – Action
 - Amy to send BMS an email re: capacitors on order.
- Peter will be absent from the office next week.
- Amy Fink**
Peter – Question
Amy – Answer
 - Worked on budgeting information needed for Peter's meeting in Victoria next week.
 - How close are we to budget release?
 - A couple of days.
 - Compiling Baseline Board component information for BMS.
 - Double-checking part numbers and lead times.
- Dave
 - We now have a PO for the EV Kit.

Socorro

- Bruce Rowen** • Tested memory leak and worked on setting up a 2nd CMIB.
- Kevin Ryan** • 90% complete: Gbit Ethernet GUI; mostly testing and resolving CMIB issues.
 - Now have a second CMIB to work with.
- 80% complete: Recirculation Controller GUI.
- 10% complete: Raw Register GUIs.

Jodrell Bank

- Simon Garrington** • Simon requests that Amy forward a copy of the revised budget once it is done.
- Peter → Should get quotes on a per/stage basis.
- Brent → Currently we are applying an estimate quote equation provided by BMS.
- Simon – Question • When will overall schedule changes be complete?
- Amy – Answer → There is a meeting scheduled for tomorrow to review the changes; should be on the web by late Thursday afternoon.