EVLA Weekly Meeting Notes

Meeting Date: 22DEC2004 Meeting Participants: Penticton: Amy, Brent, Dave, Donna, Mark, Peter, Ralph, Zhang, Zoran Socorro: Tom

General Discussion – Penticton:

DRAO:	
Goodies	 Amy brought treats; Zhang's turn next meeting, January 5, 2005.
	 Correlator System PDR is tentatively scheduled for the beginning of June.
	 Peter has a list of milestones he would like to go over in the Project Issues meeting.
Holiday	DRAO are on holiday until January 4, 2005.
	 Jodrell Bank folk are on holiday until January 5, 2005.
Contract	Correlator Chip ASIC Bid:
	\rightarrow Manufacturer has been chosen but will remain confidential until a contract has been finalized.
	\rightarrow The paperwork has been forwarded to PWGSC.
	\rightarrow Need authorization for increased funding before going ahead.
Brent Carlson:	
Comments	Completed Gbit chip functional testing.
	 Completed a first pass at the place and route; should be complete in January.
	 Worked on the correlator chip bid evaluations.
	Next tasks:
	\rightarrow Reliability study.
	→ RFP for circuit boards; Telcordia specs may prove to be the most important to include.
Dave Fort:	
Comments	Worked on the filter chip with little progress.
Question (Brent)	 Does it place and route at speed?
Answer (Dave)	Yes, working on knocking down slices.
Question (Peter)	When will the FIR chip design be done?
Answer (Dave)	 Will it ever be done? The design is on the edge so is constantly changing.
Brent	Need to have a final design that can be tested.
Dave	 Mostly working on iterations of place and route; coding and preliminary place and route is done.
Question (Peter)	 Is there a need to identify post-place and route tasks?
Answer (Amy)	 There is a consultation task that could be used for de-bugging
Zoran Ljusic:	
Comments	Reviewed Zhang's board.
	Made some changes:
	\rightarrow Simulation re: MCB bus, transistors, and serial strategy.
	Cooling of chips:
	\rightarrow Found a company with a new heat sink – will follow up.
	\rightarrow Did some investigating re: how to do without heat sinks.
	\rightarrow Has been discussing with Dave and Brent.
Dave	\rightarrow Still communicating with Breconkidge.

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Mark Halman:	
Comments	 Put together sketches of racks in 3 schemes and tested as follows:
	\rightarrow Fans in tandem – ruled out.
	\rightarrow 12 fans bolted together and mounted in a hole in the roof – temperature stabilized more or less.
	\rightarrow Next, will block 3 fans and test with 9 fans working.
	Test results:
	→ The hottest spot is in the top corners as expected and above the central support bar in the middle
	These can be designed out.
	\rightarrow Worse case at ΔT at 4w/chip = 17 °C.
	\rightarrow Worse case at ΔT at 6w/chip = 27 °C.
Ralph	 There is no problem with the cables influencing air flow.
Brent	 Once we have a drawing it should be submitted to Guy Stanzione for comment. Need to consider safety re: replacement of failed fans.
Ralph	 Will move the switches, it looks like a good simple design.
Peter	• Looking into cooling designs for rooms. Had a discussion with Tom Morgan who knows someone who may be able
	to provide some information. Need to consider over-pressure factors.
Brent	Once the physical layout is in place, things may change. Currently working with 1200 CFM/rack.
Peter Dewdney:	
Comments	Worked on correlator chip bid evaluations.
	\rightarrow Writing a report to justify the need for more funding for increased cost.
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