

EVLA Weekly Meeting Notes

Meeting Date: 10Nov2004
Meeting Participants:
 Penticton: Amy, Dave, Mark, Ralph, Rick, Sonja, Zhang, Zoran
 Socorro: Tom, Bruce
 Jodrell Bank: Bryan

Schedule Discussion – Penticton:

Dave Fort:

- Does the remaining 34h left on the Virtex IV Investigations seem an appropriate amount of time?
 → Yes, about right.

Zoran Ljusic:

- Please enter time.

Sonja Vrcic:

- Please enter time.

Ralph Webber:

- Is the rework/x-ray equipment design rules document really finished?
 → No, not done. Need to talk to Brent about it still, found some information to possibly add. Add just a few hours.

Mark Halman:

- Is the building the thermal test rack task actually complete?
 → No, need a couple more days at least.

General Discussion – Penticton:

DRAO:

Event

- Dave brought cookies.

Dave Fort:

Comments

- Did some power measurements on the filter chip with the Xilinx guy: tested stage II, a little lower power than spreadsheet estimated and power for stage I is in the lower end of the possible range. Therefore the chip is still in the 7W range.
- Have thought of a way to test power of the whole chip.
- Spent some time on autocorr chip.
- After this meeting supposed to discuss possible changes to the Fibre Optic Receiver Module.

Zoran Ljusic:

Comments

- Got input chip to the point that it can be hooked up to the station board.

Zhang Heng:

Comments

- Working on baseline board schematic more.
- Changed the recirculation controller to a Stratix II part. Going to make part for Stratix II.

Sonja Vrcic:

Comments

- Memo 18 is basically finished and will hopefully release today for informal review.

Ralph Webber:

Comments

- Worked on rework design rules document. Brent asked for more specific guidelines for rules for manufacturability so worked on that more.
- Got sub-racks. Helped Mark install them. Have to reinforce them.

Mark Halman:
Comments

- Helping AI Takasaki with more x-ray specs.
- Done some lab setup.
- Worked on ACSIS a bit.
- Spent whole week adjusting boards to fit in racks.
- Reinforced racks.
- Made a test jig to test performance of heat sink. A bit better than published data.

Schedule Discussion – Socorro:

Bruce Rowen:

- Please enter time.

General Discussion – Socorro:

Tom Morgan:

Comments

- Now writing legitimate FITS IDI files. Can be read through various inputs in AIPS.

Bruce:

Comments

- Mostly did VLBA work.