| Meeting Date: | 19-Mar-03 |
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| Meeting Attendants: | |
| Penticton: | Brent, Zoran, Zhang, Dave F, Amy |
| Socorro: | Bruce, Ken |
| Jodrell Bank: | Simon, Roger, Bryan |

Penticton

| General No | otes: | |
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| | Events | Sonja (software engineer) will almost for sure be starting on Apr. 1. (Will set a date for the correlator training meeting as soon as this is confirmed). We will have a summer student starting May.12? working on test case generation and correlator chip. |
| | Comments | The VSI connectors on the station board input/output that was talked about last week fits into phase II. Haven't heard much from ATML on the correlator chip yet. |
| Brent: | | |
| | Accomplishments | Still working on recirculation controller: have phase generator section built now. |
| Dave F: | • | |
| | Accomplishments | Same as last week but a little further. Working on FIR filter chip RFS is almost complete. |
| Zoran: | | |
| | Accomplishments | Simulated interface for delay module |
| | Comments | Problem with data between SDRAM and Xilinx chip. The Xilinx chip is somewhat fragile, it can't have overshoots or undershoots. Managed to simulate good data (no overshoot/undershoot), but this still gives only a small margin for actual board. Talked to FAE about it. Once this is solved, will finish design for layout person. |

| Zhang: | | |
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| Ac | complishments | Finished first draft of M/C mezzanine card. |
| | • | Now working on HDL code for PCMC FPGA design. |
| Ac | etion | Should send a copy of the first draft for the M/C mezzanine card to Bruce. |

Socorro

| Bruce: | |
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| Accomplishments | Still working on device driver document |

Jodrell Bank

| General Notes: | |
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| Accomplishments | Big thing for us last week was the deadline for receiving bids on optical fibre for e- MERLIN. Have got some viable bids for dark fibre. Believe we should be able to connect to all stations. |