

<b>Meeting Date:</b>	<b>01-Oct-03</b>
<b>Meeting Attendants:</b>	
<b>Penticton:</b>	Amy, Dave, Peter, Sonja, Zhang, Zoran
<b>Socorro:</b>	Bruce, Tom
<b>Jodrell Bank:</b>	Simon

## Penticton

<i>General Notes:</i>	
Events	<ul style="list-style-type: none"> <li>● Zhang brought muffins. Next week is Brent's turn.</li> <li>● Xilinx came around with new set of tools. Gave us xxt for free.</li> </ul>
Accomplishments	<ul style="list-style-type: none"> <li>● Did some review of Correlator chip RFP, quite a bit of changes.</li> </ul>
<i>Brent:</i>	
Absent	<ul style="list-style-type: none"> <li>● Gone to ALMA CDR.</li> </ul>
Accomplishments	<ul style="list-style-type: none"> <li>● Seemed to have been spending time on pad on his circuit board. Adding inductances to slow the edges...According to Dave.</li> </ul>
<i>Dave:</i>	
Accomplishments	<ul style="list-style-type: none"> <li>● Alternating between filter chip timing &amp; functional test and autocorr chip RFS.</li> <li>● The wideband autocorr chip RFS now with top-level verilog design and is going well. Shouldn't be long before RFS is done.</li> </ul>
<i>Zoran:</i>	
Accomplishments	<ul style="list-style-type: none"> <li>● Finished changes regarding delay board. Sent to Lawrence, waiting for results.</li> <li>● Did back annotation to chip design based on changes made to board.</li> </ul>
Action	<ul style="list-style-type: none"> <li>● Finally going to update delay module RFS.</li> </ul>
<i>Zhang:</i>	
Accomplishments	<ul style="list-style-type: none"> <li>● Yesterday finished PCMC PCB design simulation. Waiting for Lawrence to add logo.</li> </ul>
Comments	<ul style="list-style-type: none"> <li>● Next week review PCB design before sending to manufacturer.</li> <li>● 2 to 3 days then finished station data fanout board RFS.</li> </ul>

	<ul style="list-style-type: none"> <li>Talked to connector manufacturer about putting connector on both sides to make PCB design simpler. Hoping to get samples next week.</li> </ul>
<i>Sonja:</i>	
Accomplishments	<ul style="list-style-type: none"> <li>Divided document into 2 parts: VCI protocol spec and MCCC RFS.</li> </ul>
Deadline	<ul style="list-style-type: none"> <li>Hoping to be done soon (next week) so it can be reviewed before software meeting in Socorro.</li> </ul>
Action	<ul style="list-style-type: none"> <li>Need preliminary agenda for meeting by end of this week.</li> </ul>
Comment	<ul style="list-style-type: none"> <li>Would like to review documents. Hopefully get someone to give presentation on Monitor and Control.</li> </ul>
<i>Peter:</i>	
Comment	<ul style="list-style-type: none"> <li>Peter would like to at least see a presentation on overall architecture at software meeting. However Tom says there is not much different than what was shown in January. Peter should contact Bill Sahr and ask him to do it or get someone to do it.</li> </ul>
<i>Amy:</i>	
Accomplishments	<ul style="list-style-type: none"> <li>Updating all WBS files with information gathered from meetings in the last couple weeks.</li> </ul>
Action	<ul style="list-style-type: none"> <li>Will put new versions on the web and send out email notice. End of this week or early next week.</li> <li>Send hardware portion of WBS to Dave, Brent, Zhang, Zoran so that they can make a new preliminary parts list in preparation for another planning meeting next week.</li> <li>Send Peter a copy of Tom's backend RFS.</li> </ul>

## Socorro

<i>Tom:</i>	
Accomplishments	<ul style="list-style-type: none"> <li>Finished re-indexing .</li> </ul>
Comment	<ul style="list-style-type: none"> <li>Next: look at I/O performance issues.</li> </ul>
Question(from Peter)	<ul style="list-style-type: none"> <li>Have you considered expansion into pulsars and RFI excision.</li> </ul>

Response	<ul style="list-style-type: none"> <li>Software for these issues are not there now, but hooks have been left in software to insert those algorithms. This is not written in the RFS right now, but could be addressed if necessary.</li> </ul>
<i>Bruce:</i>	
Comment	<ul style="list-style-type: none"> <li>WBS last week was really helpful in organizing stuff.</li> </ul>
Accomplishments	<ul style="list-style-type: none"> <li>Almost finished baseline board prototype software RFS. Need a few more details from Brent.</li> <li>Moving on to TIMECODE generator box prototype software RFS.</li> </ul>

### **Jodrell Bank**

<i>General Notes:</i>	
Comment	<ul style="list-style-type: none"> <li>Bryan, Roger, Dave, and Simon are coming to Socorro. Will arrive Saturday and leave Thursday.</li> <li>Roger has agreed to give presentation on correlator architecture and M/C software architecture for e-MERLIN.</li> </ul>
<i>Simon:</i>	
Action	<ul style="list-style-type: none"> <li>Would like the excel file version of the WBS from Amy by early next week. Then Roger can look at it and comment.</li> </ul>
Comment (from Peter)	<ul style="list-style-type: none"> <li>Should discuss e-MERLIN requirements and add any addition task to WBS.</li> </ul>
Comment	<ul style="list-style-type: none"> <li>Simon can give an overview presentation of what e-MERLIN is at for Socorro meeting.</li> </ul>