

Main +5 V
Power Switch

+5 V Power Supply

Test
Switch

7-seg
LEDs

Test
FPGA

Test
Socket

EVLA ASIC Standalone Test Board Operating Instructions:

1. Turn Main +5 V Power switch OFF. Turn Test Switch OFF.
2. Plug the +5 V isolated (laptop) power supply into 110 VAC using supplied line cord.
3. Turn main +5 V power switch ON. 7-segment LEDs should read "00".
4. Open Test Socket.
5. Drop BGA F672 EVLA ASIC into the test socket, **ensure** pin A1 in the upper left (also printing on chip aligned with printing on lower right of test board). Tap gently with finger to ensure chip is set in place properly.
6. Close and latch test socket.
7. Turn Test Switch ON. If tests pass, ALL LEDs should be GREEN; 7-seg LEDs must read "29".
--If any LED is RED, turn OFF Test Switch, open socket and re-seat ASIC; repeat test.
8. Turn OFF Test Switch. Un-latch and remove ASIC.
9. Repeat steps 5-8 for the next ASIC to test.

Notes:

- It is not necessary to turn OFF the Main +5 V Power Switch between tests: a) once the test is complete (~50 msec), signals and power to the ASIC are de-energized; b) when the test switch is OFF (7-seg="00"), signals and power to the ASIC are de-energized (the FPGA is reset).
- Power-cycling with the Main +5 V Power Switch re-boots the test FPGA from ROM.
- Turning the Main +5 V Power Switch OFF removes all +5 V power from the board, but the laptop power supply is still energized.