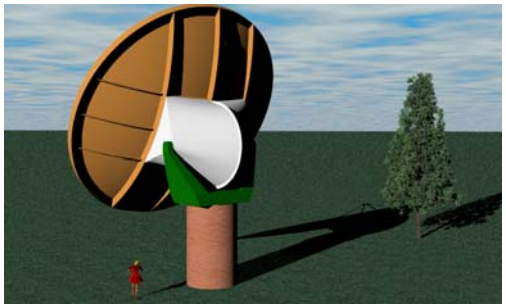

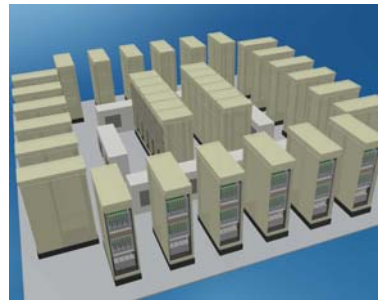

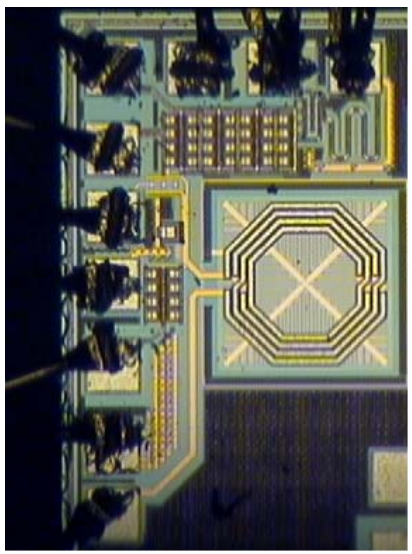

New Connectivity Scheme Schedule & Budget Impacts

Dominion Radio Astrophysical Observatory
Herzberg Institute of Astrophysics
National Research Council

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Budget Impact

- First order impact will be “positive”:
  - Few baseline boards needed, but each one is more expensive.
  - Net result is saving.
- In general this puts the project on a healthy, but not flush position.
- Greater system simplicity means less need for contingency.
- “Technical Difficulties”:
  - Most involve time (schedule) and/or reduced performance, but possibly some funds.
- Worst budget hit might be re-spin of boards.

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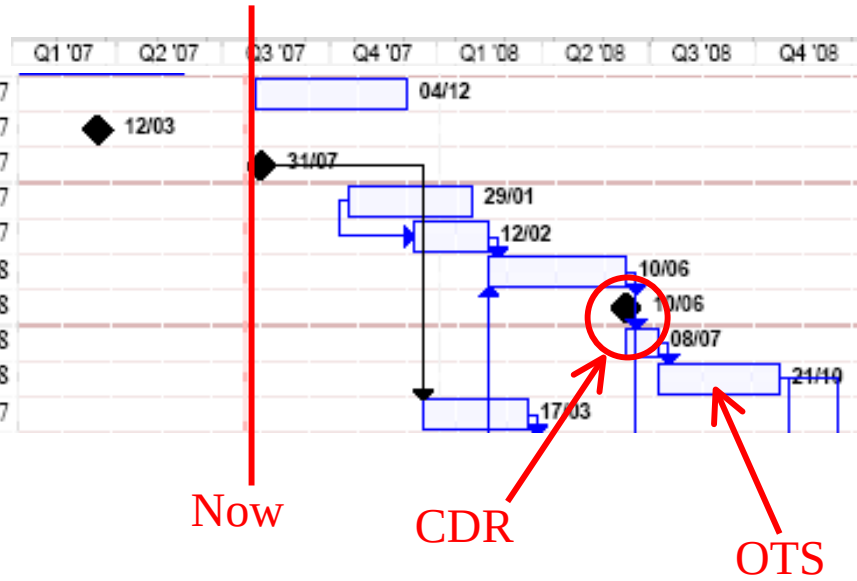
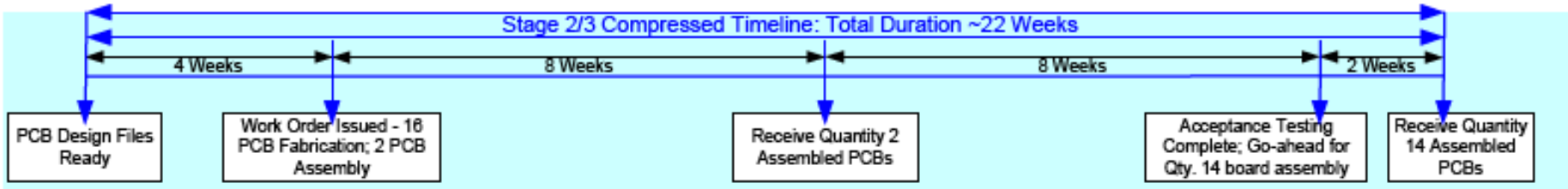
Schedule Impact

- Schedule impact of re-design on BB has already been absorbed.
  - Combination of fixes and features for new connectivity scheme.
  - BB Re-spin required anyway.
- Decisions on BB re-design required in advance of Connectivity Review to avoid undue schedule loss.
  - Current BB design can still be used in old connectivity scheme (backwards compatible).
- Technical difficulties will incur some schedule hit.
  - None of the difficulties on the list require complete re-spin of stage 2/3 build.
- Must have immediate feedback on New Connectivity Scheme because we must make final decisions on key components (FPGA's).

“Compressed” Schedule (Best Case)

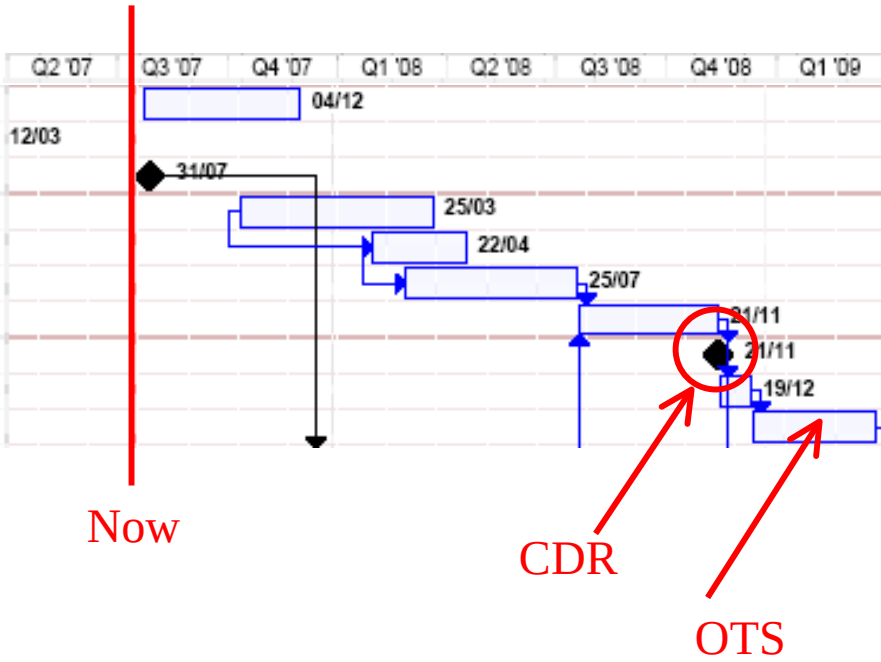
Stage 2 Prototype Fabrication (16 PCB) & Assembly (2 PCB)	27/07/2007
Power Plant Delivered to VLA Site	12/03/2007
New Connectivity Scheme Sign-Off	31/07/2007
Stage 2 Prototype Fabrication Acceptance Testing (2 Assembled PCB)	15/10/2007
Stage 2 Accepted; Go-ahead Stage 3 (Asssemble 14 PCB)	10/12/2007
Hardware/Software Integration Testing	13/02/2008
Critical Design Review	10/06/2008
Hardware for OTS Testing Sent to VLA	11/06/2008
On-The-Sky Testing	09/07/2008
High-Speed Inter-Rack Cables Delivered to and Installed at VLA Site	18/12/2007

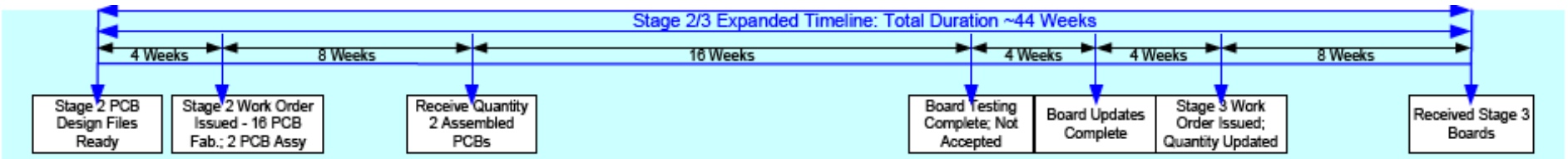




“Expanded” Schedule

Stage 2 Prototype Fabrication (16 PCB) & Assembly (2 PCB)	27/07/2007
Power Plant Delivered to VLA Site	12/03/2007
New Connectivity Scheme Sign-Off	31/07/2007
Stage 2 Prototype Fabrication Acceptance Testing (2 Assembled PCB)	15/10/2007
Stage 2 Not Accepted; Design Updates	04/02/2008
Stage 3 Work Order Issued & Hardware Built	03/03/2008
Hardware/Software Integration Testing	28/07/2008
Critical Design Review	21/11/2008
Hardware for OTS Testing Sent to VLA	24/11/2008
On-The-Sky Testing	22/12/2008





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Note on CDR Timing

- Current plan is to hold CDR
  - Before the OTS tests,
  - After the DRAO “hardware/software integration” tests.
- This provides the CDR committee with the opportunity
  - to review lab performance in its entirety and possibly suggest extensions.
  - to review suggested “critical OTS” tests and suggest alternatives or additional tests.
- CDR committee will be informed of OTS results by email and asked for a quick ratification.
- CDR committee makes recommendations, not final decisions.

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End