



VLA  
EXPANSION  
PROJECT

# Requirements and Additional Capabilities



# Outline

- “Dream” correlator requirements comparison.
- Additional capabilities.
- Other requirements.

Description	“Dream” Correlator Spec.	Planned deliverable
<b>No. of antennas</b>	36	
<b>Bandwidth</b>	4 x 2 x 2 GHz (16 GHz)	
<b>Freq. Resolution</b>	few Hz ... 10’s MHz	
<b>No. of independently tunable IF pairs</b>	at least 4, prefer 8	
<b>No. of frequency channels</b>	1000 (full polarization per IF pair), 8000 total	
<b>Frequency channel flexibility</b>	split flexibly among IFs,  select subset for writing	
<b>Flexibility</b>	Frequency resolution: factors of 2 Flexible tradeoffs (#baselines, B.W., #channels, pol’n, time res’n) Interf. sub-arrays: 4 independent Phased sub-arrays: 4 independent	
<b>Integration times</b>	0.1 sec (less with tradeoffs)	
<b>Total data rates</b>	few tens of Mvis/sec	
<b>Autocorrelations</b>	all stokes parameters	
<b>RFI</b>	as many channels as possible 10 <sup>6</sup> dynamic range  automatic flagging  gating	
<b>Pulsar phase binning</b>	up to 1000	
<b>Phase Cal</b>	at least auto-spectra	
<b>Delay tracking</b>	1/16 <sup>th</sup> sample, 250 km baseline	

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<b>Freq. Resolution</b>	few Hz ... 10’s MHz	1 Hz ... 2 MHz
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Delay tracking	1/16 <sup>th</sup> sample, 250 km baseline	digital ±1/32 <sup>nd</sup> sample, 10 <sup>4</sup> km+ bl

## Additional Capabilities

- With front-end switch, able to multi-beam/multi-process basebands (IFs).
- Sub-band multi-beaming within  $\sim 0.25^\circ$  of baseband beam (all up to 2200 km baselines, 1 per baseband up to 10k baselines).
- VLBI-ready.
- Digital phased output...expandable to phase up to 16 GHz.
- Multiple digital sub-bands that are width, number of channels, and placement flexible.
- Flexible baseband width inputs...useful for phased-VLA corr.
- Expandable architecture.
- Tradeoff number of antennas for bandwidth (2X, 4X)
- Future “cheap” upgrade path (#ants, #channels).

## Other Requirements

- **Change configurations in ~1 second.**
- **Sub-band reject-band attenuation.**
  - No specification, plan 12-bit LUT, should be ok for -60 dB.
  - Smaller LUT, can be traded off for more taps. FPGA programmable.
- **Aliasing attenuation.**
  - No specification, but amplitude  $\sim 10\log(1/(12f_{\text{shift}}T))$ ; T is the *incoherent* integration time.
    - T=10 msec;  $f_{\text{shift}}=10$  kHz : -30 dB.
    - T=100 msec;  $f_{\text{shift}}=10$  kHz : -40 dB.
    - T=10 msec;  $f_{\text{shift}}=1$  kHz : -20 dB; T=100 msec : -30 dB.
  - ...and a factor of two sensitivity loss at the sub-band boundary. Some control over SNR loss region possible with different filter shapes depending on requirements.
- **Digital passband ripple.**
  - Should be small (~1 dB?) to minimize requantization sensitivity losses. Can tradeoff steepness of transition band for flatness of passband. “Just load in different tap coefficients”.