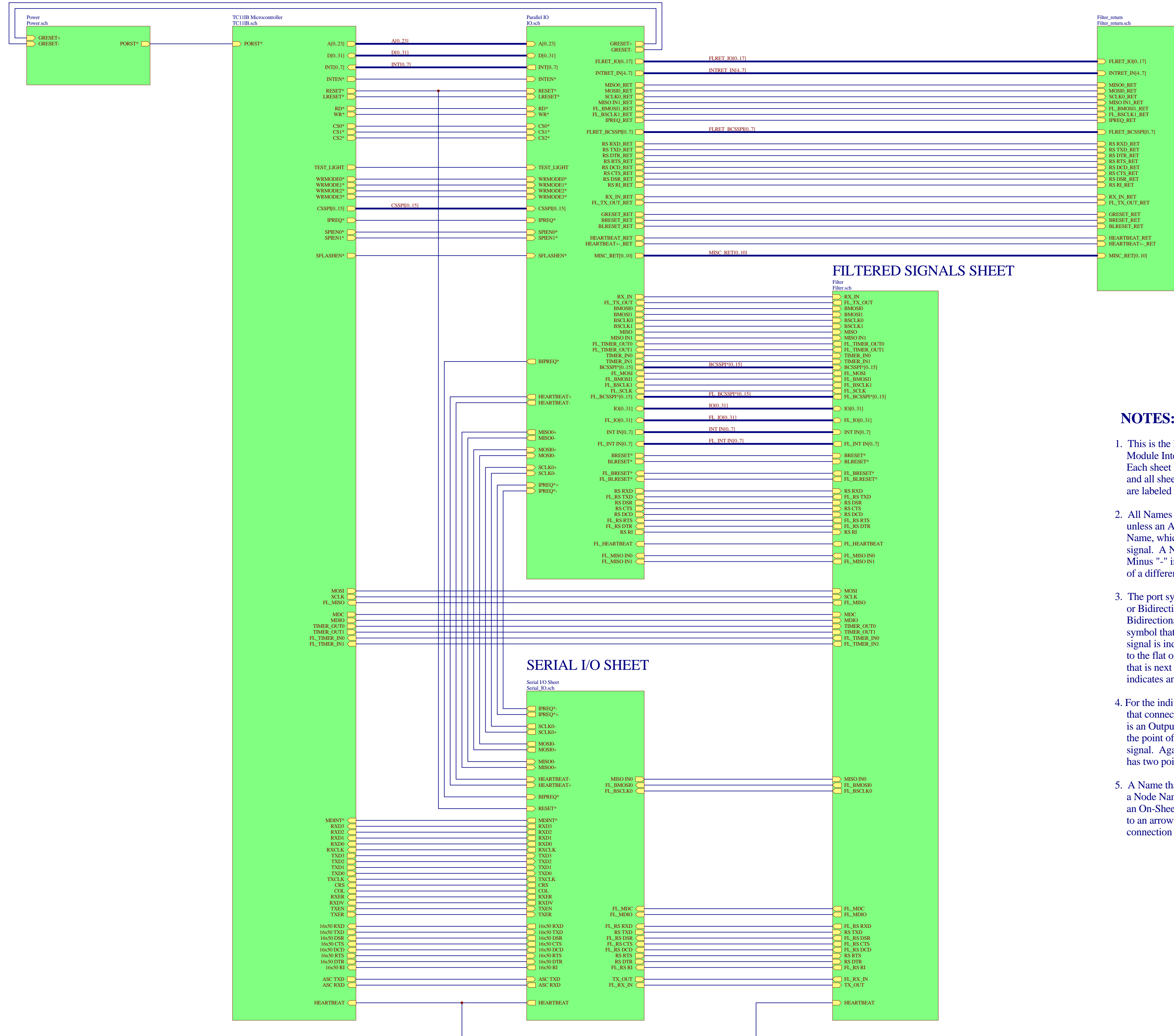


POWER REGULATOR SHEET MICROPROCESSOR SHEET

PARALLEL I/O, BUFFER, AND CONNECTOR SHEET

FILTERED RETURNS SHEET



NOTES:

- This is the hierarchical sheet for the Module Interface Board (MIB). Each sheet is designated by a block and all sheet-to-sheet connections are labeled with a unique Name.
- All Names are considered High-True, unless an Asterisk "*" appears in the Name, which then indicates a low-true signal. A Name that has a Plus "+" or Minus "-" indicates a signal that is part of a differential pair.
- The port symbol indicates Input, Output, or Bidirectional signal paths. A Bidirectional signal is indicated by a port symbol that has two points. An Output signal is indicated when the Name is next to the flat of the port symbol. A Name that is next to the point of the port symbol, indicates an Input signal.
- For the individual schematic sheets, a line that connects to the flat of the port symbol is an Output signal, while a connection to the point of a port symbol indicates an Input signal. Again, a Bidirectional port symbol has two points.
- A Name that appears above a line indicates a Node Name that is generally used for an On-Sheet connection. A Name next to an arrow symbol indicates a power connection that is valid for all sheets.

Title		
Size	Number	Revision
D		
Date:	17-Aug-2004	Sheet of
File:	D:\Print_work\MIBR4\MIBR4.dtb	Drawn By: